**Job Title: Wireless Baseband Design Engineer**

**Location: Gent-Belgium**

**Company:** imec – University of Gent, Better Than Wired initiative

**About Us:** Better Than Wired is an innovative startup initiative focused on revolutionizing wireless communication systems. It is based on a core research team of imec and University of Gent, aiming to transform research to useful innovative new products. We are looking for a talented and motivated Wireless Baseband Design Engineer to join our dynamic team. If you are passionate about cutting-edge technology, wireless communication, and FPGA hardware design, we want to hear from you.

**Position Overview:** As a Wireless Baseband Design Engineer, you will play a pivotal role in the development of OFDM-based wireless systems. You will be responsible for designing and implementing baseband processing algorithms, creating hardware designs in Verilog and/or VHDL, and utilizing Xilinx tools for FPGA development. You will also be implementing the needed PHY API towards the device driver as a Linux kernel module. This is an exciting opportunity to be part of a fast-growing startup and contribute to the future of wireless technology.

**Key Responsibilities:**

- Design and implement baseband processing algorithms for OFDMA-based wireless systems.
- Develop hardware designs using Verilog and/or VHDL for FPGA implementation.
- Collaborate with cross-functional teams to integrate and optimize baseband processing solutions.
- Conduct performance analysis and optimization to meet system requirements.
- Debug and troubleshoot hardware and software issues.
- Stay up-to-date with the latest industry trends and emerging technologies.

**Qualifications:**

- Bachelor's or Master's degree in Electrical Engineering, Computer Engineering, or a related field.
- Strong knowledge of wireless communication systems and baseband processing principles.
- Familiarity with OFDM and wireless communication standards is a plus.
- At least 3 years of experience on FPGA developing
- Proficiency in Verilog and/or VHDL for FPGA design.
- Experience with Xilinx FPGA development tools (Vivado, Vivado HLS, etc.).
- Excellent problem-solving skills and attention to detail.
- Strong communication and teamwork skills.
- Ability to work in a fast-paced startup environment.
- Fluent in English

**What We Offer:**

- Competitive salary and possible equity options.
- Opportunities for career growth and development.
- A collaborative and innovative work environment.
- The chance to work on groundbreaking technology in the wireless communication industry.
- Flexible work hours and the option for remote work.

If you are passionate about wireless technology, have a solid background in baseband design, FPGA development, and want to make a significant impact in a startup environment, we encourage you to apply.

**How to Apply:** Please send your resume and a cover letter highlighting your relevant experience to [spilios.giannoulis@imec.be](mailto:spilios.giannoulis@imec.be) . In your cover letter, please explain why you are interested in joining our team and how your skills align with the position.

Imec is an equal opportunity employer. We celebrate diversity and are committed to creating an inclusive environment for all employees.

Application Deadline: 28/02/2025

Join us in shaping the future of wireless communication technology!